FIREBERD™ 8000

Take testing to the next level with a single platform for new and legacy requirements

Highlights

- New Version 3.0 brings graphical results, pattern slip generation, sync loss criteria and EIA-530A as standard features
- New FB-8000-ADVR option detects broken leads on incoming balanced leads and graphically reports clock vs. data phase relationships
- Generates PRBS, ANSI, ITU, and round-trip delay BER patterns with data rate support up to 18 Mb/s
- Native datacom interfaces for EIA-530, EIA-530A, RS-449/V.36, RS-232/V.24, X.21, V.35, and conditioned diphase serial interfaces
- Government standards support for MIL-188C and MIL-188-114
- Synchronous/asynchronous DTE/DCE modes of operation
- "Virtual breakout box" functionality allows flow control troubleshooting with user controllable signaling leads (CTS, RTS, DTR, RLSD, and DSR)

Version 3.0 of the popular FIREBERD 8000 advances testing functionality with features that simplify troubleshooting when maintaining data communications circuits and network elements. Version 3.0 provides technicians with the ability to further troubleshoot datacom applications past simple "go"/"no go" testing by delving into root cause analysis of circuit problems. In addition, the new FB-8000-ADVR software option provides technicians with the capability to graphically monitor clock and data phase relationships, providing insight into circuit troubles and timing issues. The FB-8000-ADVR option also provides the ability to detect if one or both leads of a balanced (differential) circuit are broken, leading to quicker problem resolution.

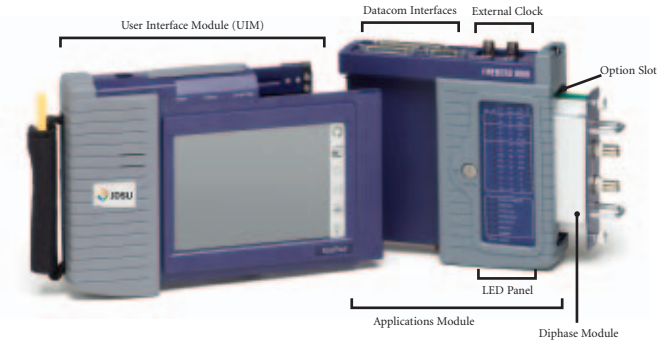
Version 3.0 offers the following standard features

– Graphical Results
  - Manual or automatic periodic refresh
  - On-screen, JPG, GIF, and PDF output

– Pattern Sync Loss Criteria
  - High, medium, low, and "never" settings

– Pattern slip generation
  - Single bit insertion

– Support for EIA-530A Interface



The new FB-8000-ADVR Option offers the following capabilities

– Clock/Data Phase Graph

The FB-8000 will graphically report the phase relationship of user-selected Data and Timing leads, allowing the user to quickly identify clock-data phase alignment issues.

– Broken Lead Detection

Some digital circuits will continue to operate correctly when one lead of a balanced (differential) circuit is broken. In these cases, the signal ground lead becomes the return current path, causing reduced fidelity. The broken lead detection feature of the FB-8000 allows technicians to detect broken incoming leads for DTE and DCE modes, allowing for more rapid circuit troubleshooting and restoration.

Quality of Service Testing

Circuits must be qualified before service hand-off to the end user. The FB-8000's results analysis, including round trip delay, G.821, and pattern slips, allows technicians to verify quickly that circuits are performing to specified metrics before they are brought into service. The FB-8000 now features a graphical histogram capability that displays all counter results in graphical format. The FB-8000 can monitor bit errors, block errors, pattern losses, clock losses, receiver data losses, and transceiver clock losses.

Ordering Information

Applications Module – FIREBERD 8000

- FB-8000
  FIREBERD Applications Module
  Includes RS-232/V.24, RS-449/V.36, V.35/306, EIA-530, EIA-530A, X.21, MIL-188C, MIL-188-114
- FB-8000-Diphase
  Includes conditioned diphase interface module
- FB-8000-CLK
  Includes Clock Recovery option
- FB-8000-ADVR
  Advanced Datacom Results
  Includes Clock/Data Phase Graphing and Broken Lead Detection options

User Interface

2000-V6
JDSU FST-2000 TestPad User Interface Module (UIM)

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